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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

File the Application of: **KIRA, et al.**

Serial No.: **08/897,953**

Group Art Unit: **2822**

Filed: **July 24, 1997**

Examiner: **David E. GRAYBILL**

P.T.O. Confirmation No.: **5157**

For: **METHOD AND SYSTEM FOR FABRICATING A SEMICONDUCTOR DEVICE**

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 3, 2006

Sir:

In response to the Office Action dated **January 13, 2006**, please amend the above-identified application as follows: